ivian Hy

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EDUCATION

M.S Industrial and Systems Engineering, San Jose State University – GPA: 3.9	June 2026	
B.S Industrial and Systems Engineering, San Jose State University – GPA: 3.9	June 2025	
Coursework: Computer Integrated Manufacturing, Service Systems Engr, Life Cycle Engr, Operations Research,		
Supply Chain Management, Statistical Analysis, Design of Experiments, Software Engr		

SKILLS

Programming: SQL, Java, C++, MATLAB, Linux, R, Simulink, Python

Tools: Minitab, ProModel Simulation, Tableau, Arduino IDE, SolidWorks, Node-RED

Technical Skills: Product Lifecycle, Data Analysis, System Simulation, Continuous Improvement, 5S Strategy, Lean Six Sigma Methodologies

EXPERIENCE

Western Digital - Procurement Intern

- Designed procurement strategies to modeled best total cost of ownership for top-spend parts, resulting in projected cost savings of 26%
- Managed RFQs and RFPs, developing KPI-driven scorecards to assess supplier performance, ensure ٠ cost-effectiveness, and quality benchmarks for 5 suppliers
- Analyzed spend data with engineering and planning teams to approve top-spend parts for Build-To-Spec sourcing and develop FY25 projected spend forecasts of 90 parts

Santa Clara Valley Water - Engineering Intern

- Optimized payment processing to minimize bottlenecks to improve financial performance by 80%
- Expedited construction plans by 15% through examining technical drawings and specifications for the Rinconada Water Treatment Plant construction project
- Facilitated cross-functional collaboration among 15 team members by documenting project milestones

Kaiser Permanente - Intern

- Implemented a classifying system through all phases of the project including design, build, testing, and deployment that resulted a 20% increase in operation efficiency
- Conducted in-depth analysis of existing processes, identifying and implementing improvements that streamlined workflows and enhanced system performance
- Skillfully led a cross-functional team of 5 to enhance a classifying system, focusing on process optimization and documentation design of experiments

PROJECTS

Arduino-Based LED and Traffic Light Simulation Project

- Utilized the Arduino platform to design and implement an interactive LED control system and simulate a basic traffic light with accurate timing and control
- Programmed Arduino Uno using C++ to blink LEDs and design circuit diagrams, optimize code for efficiency, and ensure reliable system performance

ProModel Cross-Dock Simulation, San Jose State University

- Designed optimal warehouse layout for managing produce tested various layouts and equipment configurations using ProModel Simulation Software
- Performed experimentation with simulation model, achieved a 15% reduction in average cycle time and improved efficiency in arrival and departure handling

Demand Forecasting and Capacity Planning Project

- Applied Python to implement forecasting models, including Decision Trees and Random Forests •
- Optimizatized Materials Requirement Planning models integrating Master Production Schedule and Bill of Material to formulate a production plan to maximize business objectives

System Life Cycle, San Jose State University

- Determined customer wants and needs of a restaurant seating system, developed a House of Quality for the system based on prioritization of Operational Requirements and Technical Performance Measures
- Created context diagram, interfaces, system boundaries, and a 5 year Life Cycle Cost, while implementing a Failure Modes, Effects, and Causality Analysis (FMECA)

ACTIVITES

Spartan Accelerated Graduate Education (SAGE) Scholar, SJSU	August 2024 - Present
Teacher Assistant, SJSU College of Engineering	January 2024 - Present
Member, Institute of Industrial and Systems Engineers (IISE)	August 2022 - Present
Member, MESA Engineering Program	August 2022 - Present
Mentee, Western Digital Mentorship Program: Davi Song, Information Technology	June 2021 - Present

Fall 2024

Fall 2023

Spring 2023

Spring 2024

June 2024 - August 2024

May 2023 - August 2023

May 2021 - August 2021